



Hope Foundation's
PRALHAD P. CHHABRIA
RESEARCH CENTER (PPCRC)

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Workshop on “FPGA Based System Design” 21st – 23rd March 2018

Objective:

Training on Embedded System Design and introduction to VLSI Design

Duration: 3 days

Am I eligible?

- Students pursuing B.E/ B. Tech from ECE, EI, EEE who require industrial training or projects
- Student pursuing M.E./M. Tech, M.Sc Physics/ Electronics interested in hardware design.

Workshop Focus

Each Participant will be given hands-on experience in the following:

- FPGA architecture and design flow
- FPGA based system design
- VHDL synthesis and simulation

Take-away

- Each Participant will be provided with a mini-laboratory which contains all the necessary electronic hardware, software, FPGA board and tools required during the workshop. This mini-laboratory will be the property of the participants
- Participants will be certified after successful completion of the workshop.

Fees: ₹ 8,500 (GST @ 18% payable extra)
(Hostel facilities available)

Contact Details:

Prof. Bhushan Patil – 88067 77865
Prof. Amit Patwardhan – 82378 16916
Prof. Rabinder Henry – 99237 00296
Email: info@ppcrc.in

For registration please visit: <http://www.ppcrc.in/workshop-on-fpga-based-system-design>

Last date for registration: 13th March 2018



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